Filing Date: February 22, 2000

Title: SYSTEM SUPPORTING MULTIPLE MEMORY MODES INCLUDING A BURST EXTENDED DATA OUT MODE

## **REMARKS**

This paper responds to the Office Action mailed on August 29, 2005.

No claims are amended, canceled, or added; as a result, claims 26-34 and 38-52 remain pending in this application.

## **Double Patenting Rejection**

Claims 26-34 and 38-52 were rejected under the judicially created doctrine of obviousness-type double patenting over claims 1-19 of U.S. Patent No. 6,804,760 in view of U.S. Patent No. 6,505,282. A Terminal Disclaimer in compliance with 37 C.F.R. 1.321(b)(iv) is enclosed herewith to obviate these rejections. Applicant makes no admission as to the merits of this rejection.

## Allowable Subject Matter

Claims 26-34 and 38-52 were indicated to be allowable if the above double patenting rejections are overcome as set forth in the Office Action.

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## **CONCLUSION**

Applicant respectfully submits that the claims are in condition for allowance, and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (612) 349-9587 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 22 day of November, 2005.

Signature

Name